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S174	1	09/923,242	US-PGPUB	OR	ON	2006/01/25 09:12
S175	3	(direct adj1 conversion or homodyne or zero$3IF) with receiver and modulat$3 adj1 (extreme$1 or maximum or minimum) and DC adj3 (offset or compensat$3 or remov$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 11:07
S176	117	(direct adj1 conversion or homodyne or zero$3IF) and modulat$3 with (extreme$1 or maximum or minimum) and DC adj3 (offset or compensat$3 or remov$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 11:08
S177	11	(direct adj1 conversion or homodyne or zero$3IF) and modulat$3 with (extreme$1 or maximum or minimum) with DC adj3 (offset or compensat$3 or remov$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 11:10
S178	1	(direct adj1 conversion or homodyne or zero$3IF) and inverse with filter with DC adj3 (offset or compensat$3 or remov$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 11:24
S179	47	(direct adj1 conversion or homodyne or zero$3IF) and inverse with filter and DC adj3 (offset or compensat$3 or remov$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 11:10
S180	1	(direct adj1 conversion or homodyne or zero$3IF) and inverse with function with DC adj3 (offset or compensat$3 or remov$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 11:25
S181	36	(direct adj1 conversion or homodyne or zero$3IF) and inverse with function and DC adj3 (offset or compensat$3 or remov$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/26 13:19
S182	56	(direct adj1 conversion or homodyne or zero$3IF) and inverse with (filter$3) and DC adj3 (offset or compensat$3 or remov$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/26 14:01
S183	3	(direct adj1 conversion or homodyne or zero$3IF) and inverse with (filter$3) with DC with (offset or compensat$3 or remov$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/26 13:20
S184	94	(direct adj1 conversion or homodyne or zero$3IF) and inverse with (filter$3 or function) and DC with (offset or compensat$3 or remov$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/26 14:03

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Digital Object Identifier 10.1109/PIMRC.2000.881564

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☐   2. **Adaptive DC-offset compensation algorithm for burst mode operated dire receivers**
Sampei, S.; Feher, K.;
Vehicular Technology Conference, 1992 IEEE 42nd
10-13 May 1992 Page(s):93 - 96 vol.1
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☐   3. **A fully integrated inter-stage-bandpass-filter-less direct-conversion recei**
Tamura, M.; Nakayama, T.; Hino, Y.; Yoshizawa, A.; Takagi, K.;
Radio Frequency integrated Circuits (RFIC) Symposium, 2005. Digest of Pape
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